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Case-No. 00GR01954248

DIRECTOR,
U.S. PATENT AND TRADEMARK OFFICE
WASHINGTON, DC 20231

Transmitted herewith for filing is the patent application
of:



For: DIGITAL SIGNAL PROCESSOR WITH PARALLEL ARCHITECTURE

Enclosed are:

- Patent Application: 28 pages, 19 claims.
 3 sheets of drawings.
 The suggested drawing figure to be published is FIG. 2.
 A Preliminary Amendment.
 Submission of Proposed Drawing Modification.
 A certified copy of a French application.
Assignee info:
 Name: STMicroelectronics S.A.
 Address: 29, boulevard Romain Rolland, F-92120 Montrouge, France
 Country of Incorporation: France
Applicant claims priority benefit to the following foreign application(s):
 Country: France
 Application No.: 0009882
 Filing Date: July 27, 2000

The Declaration and Filing Fee are **NOT ENCLOSED**.

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 Associate this file with Customer No. 27975.



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Date: July 26, 2001